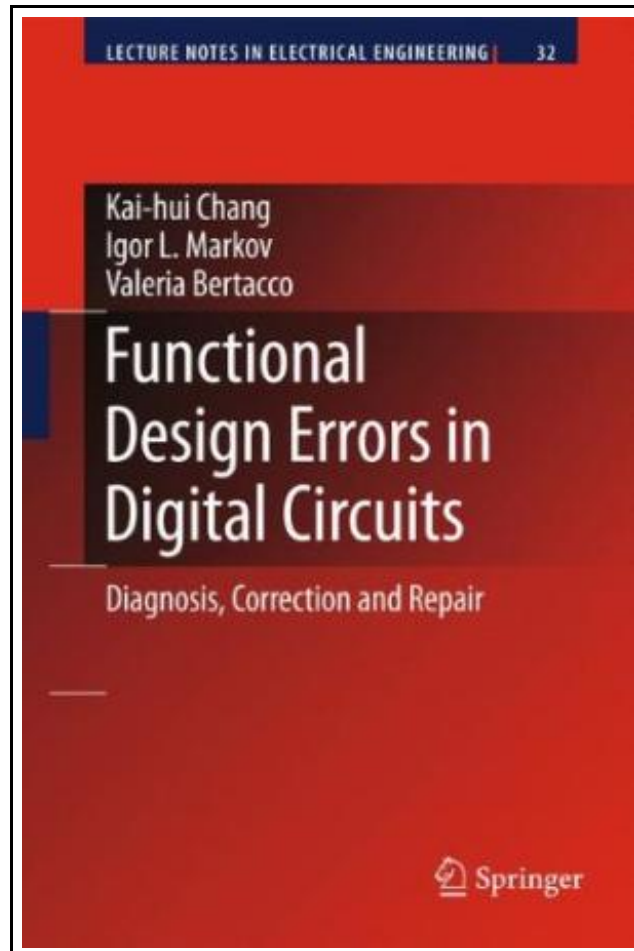# Functional Design Errors in Digital Circuits: Diagnosis Correction and Repair

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Reviews

An extremely wonderful pdf with lucid and perfect explanations. I could possibly comprehended every little thing out of this created e pdf. Once you begin to read the book, it is extremely difficult to leave it before concluding.
(Janie Wilkinson)

FUNCTIONAL DESIGN ERRORS IN DIGITAL CIRCUITS: DIAGNOSIS CORRECTION AND REPAIR

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Springer. Hardcover. Book Condition: New. Hardcover. 200 pages. Dimensions: 9.3in. x 6.2in. x 0.8in.Functional Design Errors in Digital Circuits Diagnosis covers a wide spectrum of innovative methods to automate the debugging process throughout the design flow: from Register-Transfer Level (RTL) all the way to the silicon die. In particular, this book describes: (1) techniques for bug trace minimization that simplify debugging; (2) an RTL error diagnosis method that identifies the root cause of errors directly; (3) a counterexample-guided error-repair framework to automatically fix errors in gate-level and RTL designs; (4) a symmetry-based rewiring technology for fixing electrical errors; (5) an incremental verification system for physical synthesis; and (6) an integrated framework for post-silicon debugging and layout repair. The solutions provided in this book can greatly reduce debugging effort, enhance design quality, and ultimately enable the design and manufacture of more reliable electronic devices. This item ships from multiple locations. Your book may arrive from Roseburg,OR, La Vergne,TN. Hardcover.

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